

What is claimed is:

1. A remodulator clock signal source, comprising:

a vestigial sideband demodulator, the demodulator being responsive to vestigial sideband transmissions containing timing information, the demodulator recovering the timing information; and

a signal path coupling the recovered timing information produced by the demodulator to a remodulator clock input so as to regulate the remodulator timing sequence.

2. A system comprising:

an input for receiving a modulated signal comprising timing information;

a demodulator coupled to the input for extracting the timing information;

a phase locked loop coupled to the demodulator for generating clock pulses in response to the timing information; and

a remodulator coupled to the phase locked loop for receiving the generated clock pulses.

3. The system of claim 2, further comprising a variable frequency oscillator, coupled to the phase locked loop, the variable frequency oscillator receiving a correction signal from the phase locked loop based upon the source of timing information, the variable frequency oscillator thereby having an accuracy substantially equal to the source of timing information.

4. The system of claim 3, wherein the phase locked loop further comprises:

a first closed loop operating condition characterized by the generation of the correction signal to the variable frequency oscillator based upon data from the timing information; and

a second open loop operating condition characterized by an absence of data from the timing information, thereby causing the variable frequency oscillator to operate without a correction signal.

5. The system of claim 4, further comprising:

a value register coupled to the variable frequency oscillator and maintaining a value substantially equal to the average value of the correction signal over a recent time interval; and

5 a multiplexer, the multiplexer selectively coupling the value from the value register to the variable frequency oscillator in the open loop operating condition, and coupling the correction signal from the phase locked loop to the variable frequency oscillator otherwise.

10 6. A system according to claim 2, wherein the modulated signal is a VSB modulated signal containing high definition television information.

7. A system according to claim 6, wherein the VSB modulated signal is in accordance with the ATSC standard.

15